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08/754826

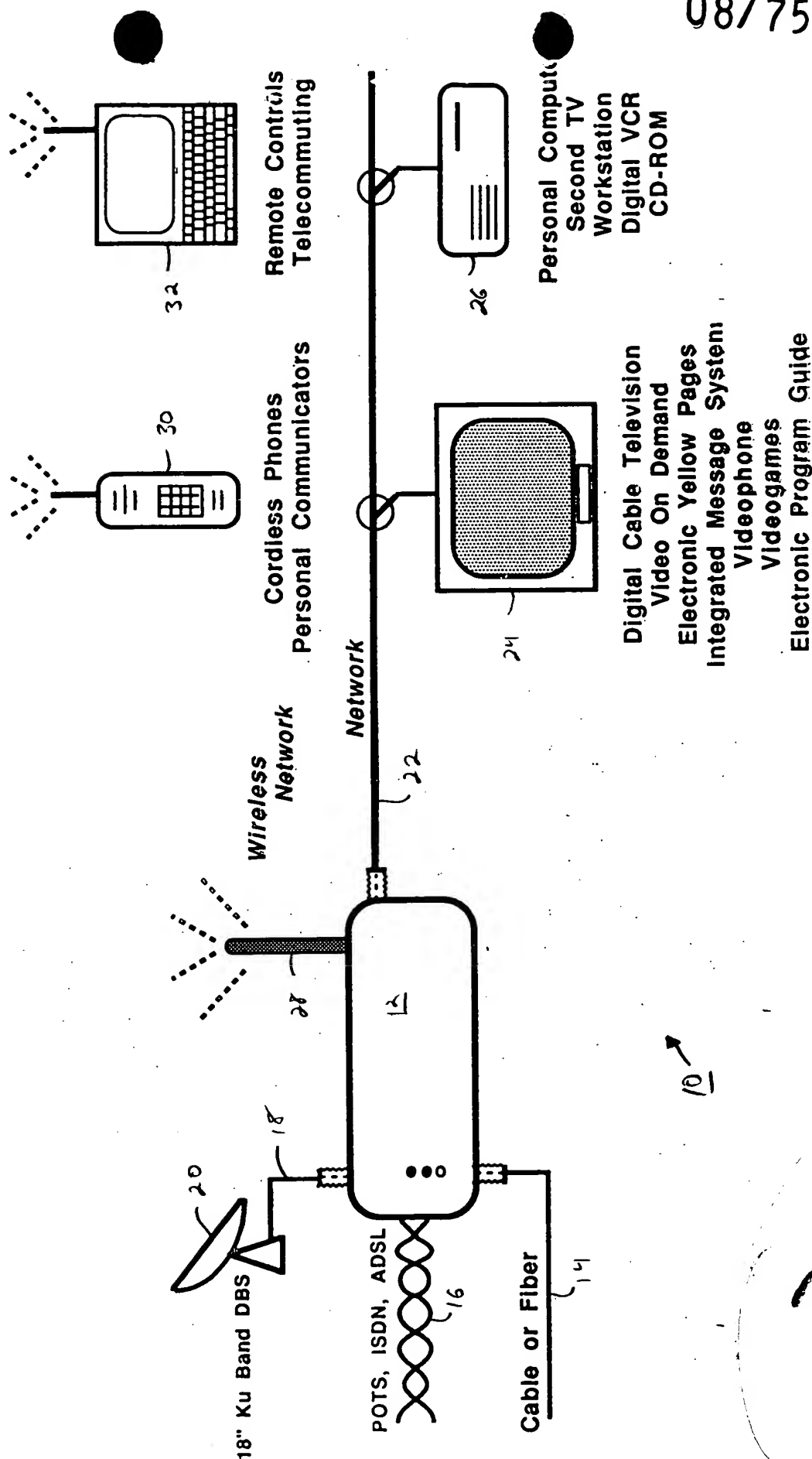


Figure 1

13

Figure 2

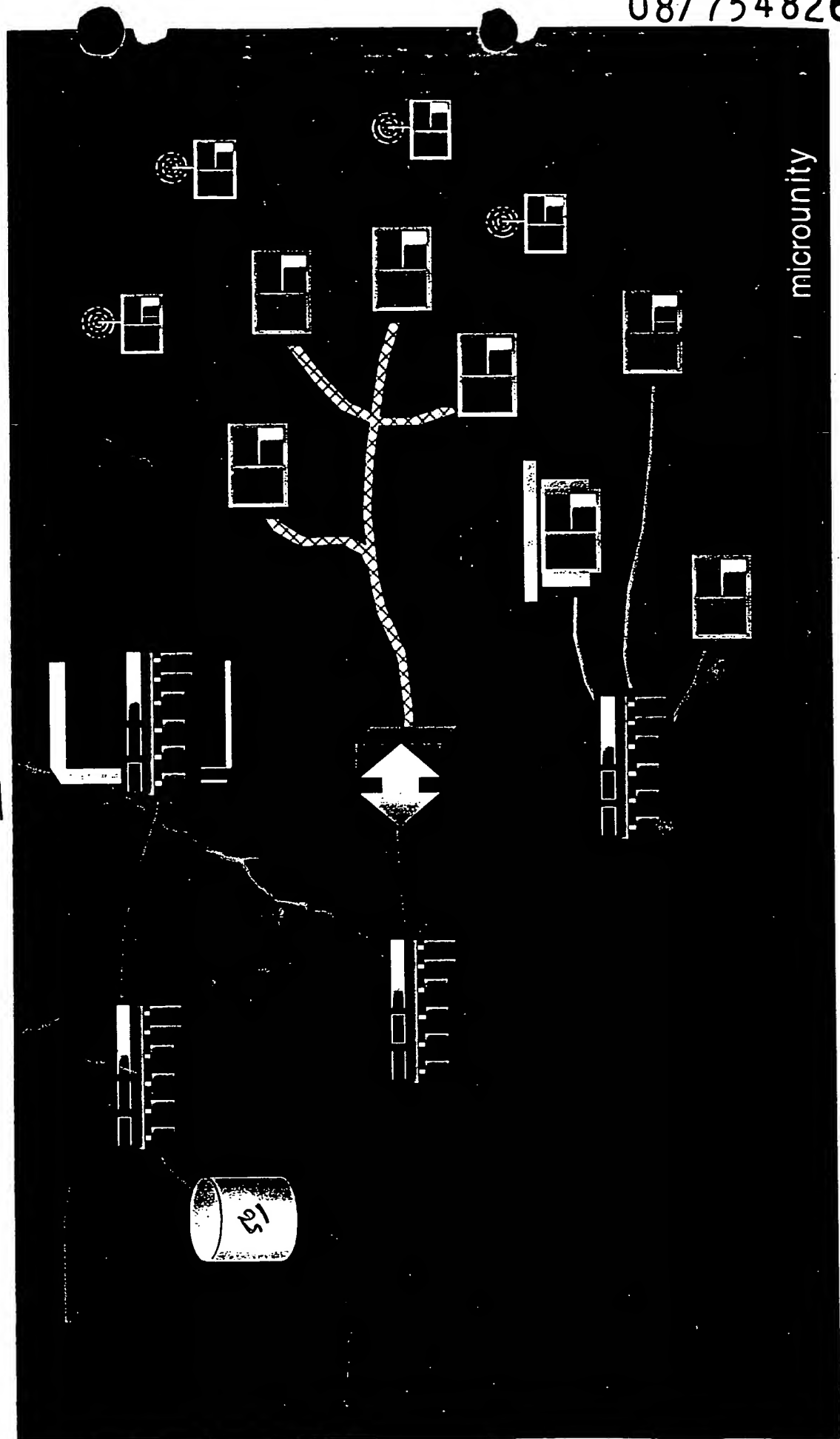


Figure 3

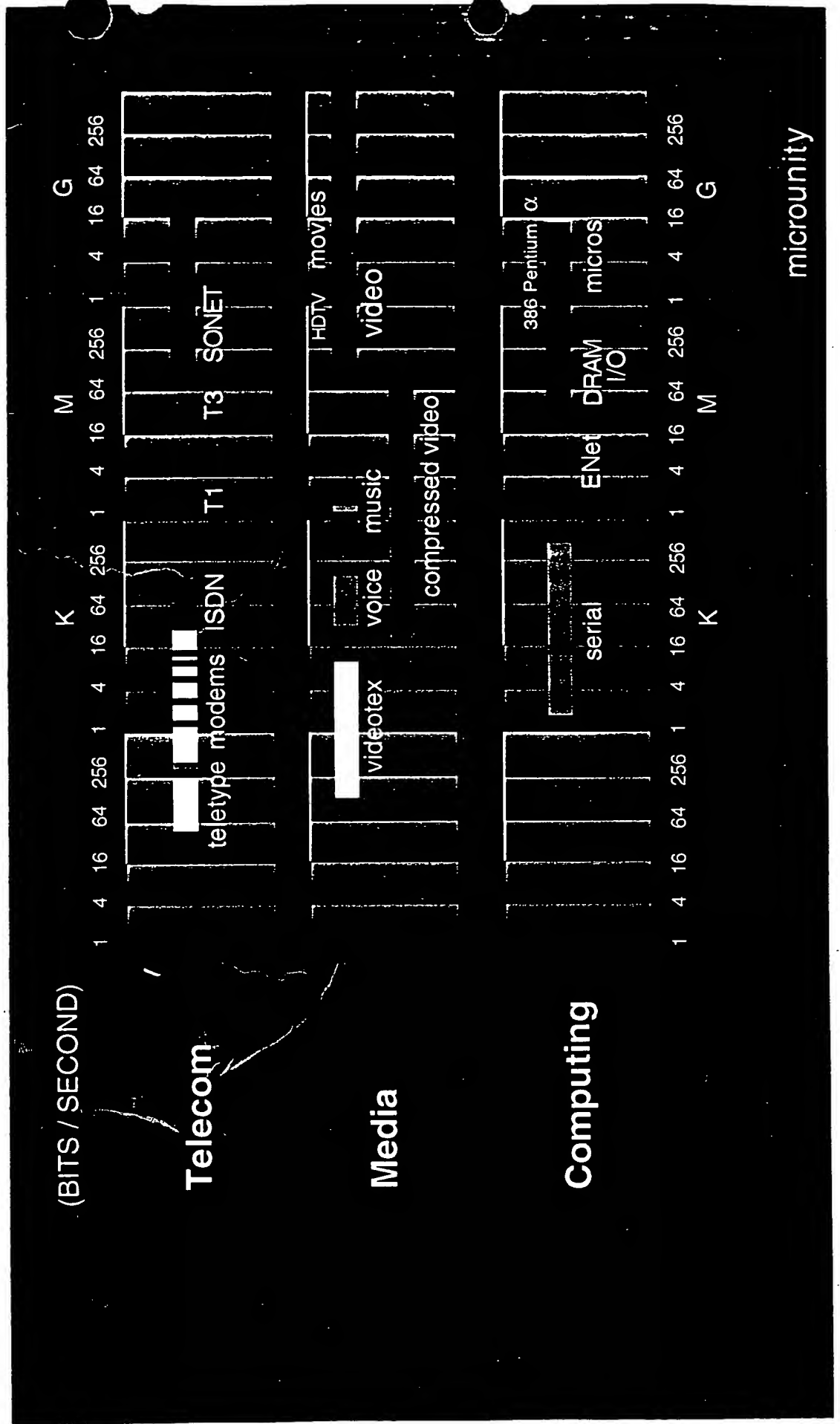
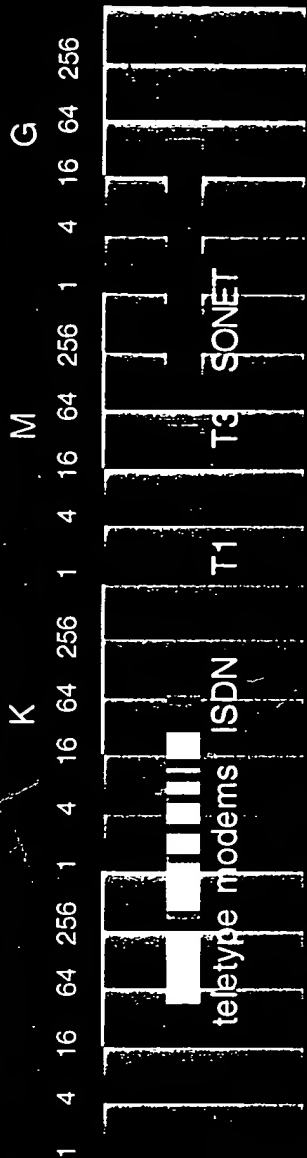


Figure 4

(BITS / SECOND)

### Telecom



### Media

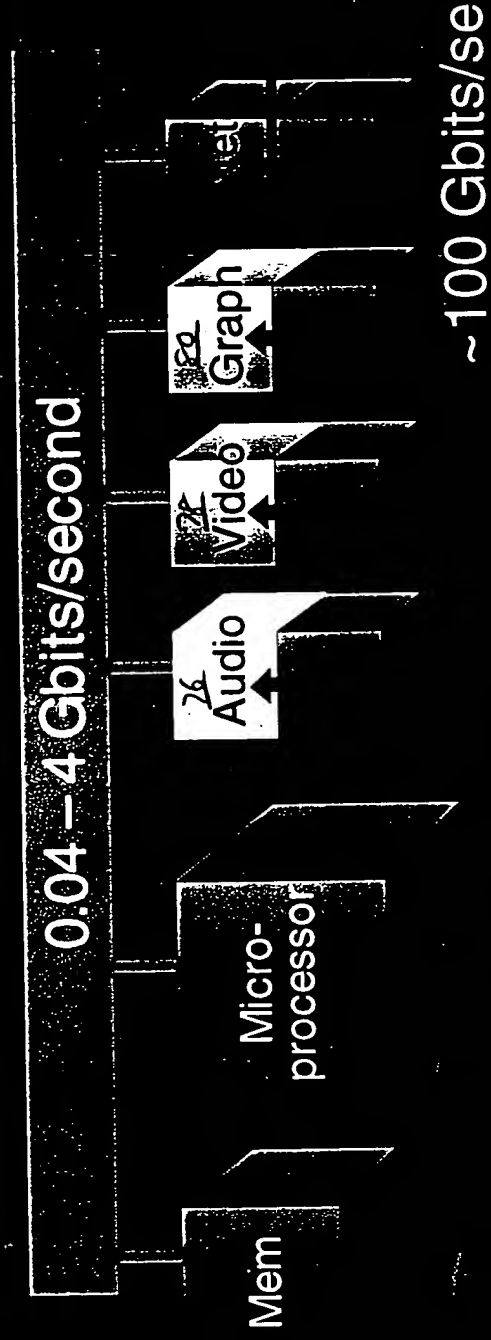


### Computing



microunity

Figure 5



The Future of the Old Way (1999)

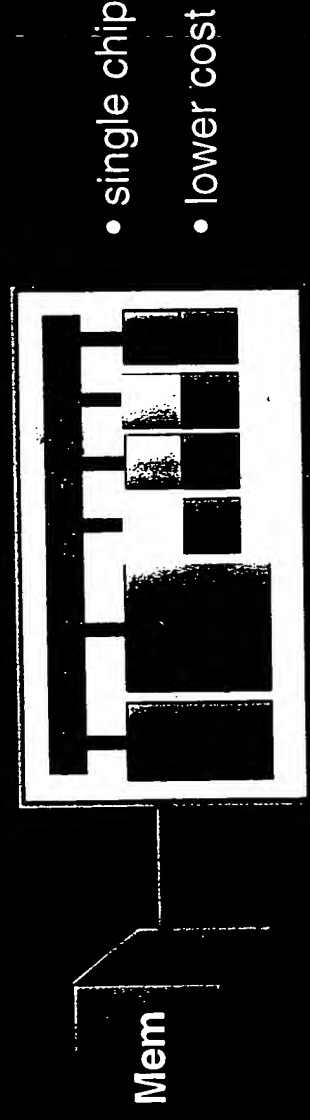
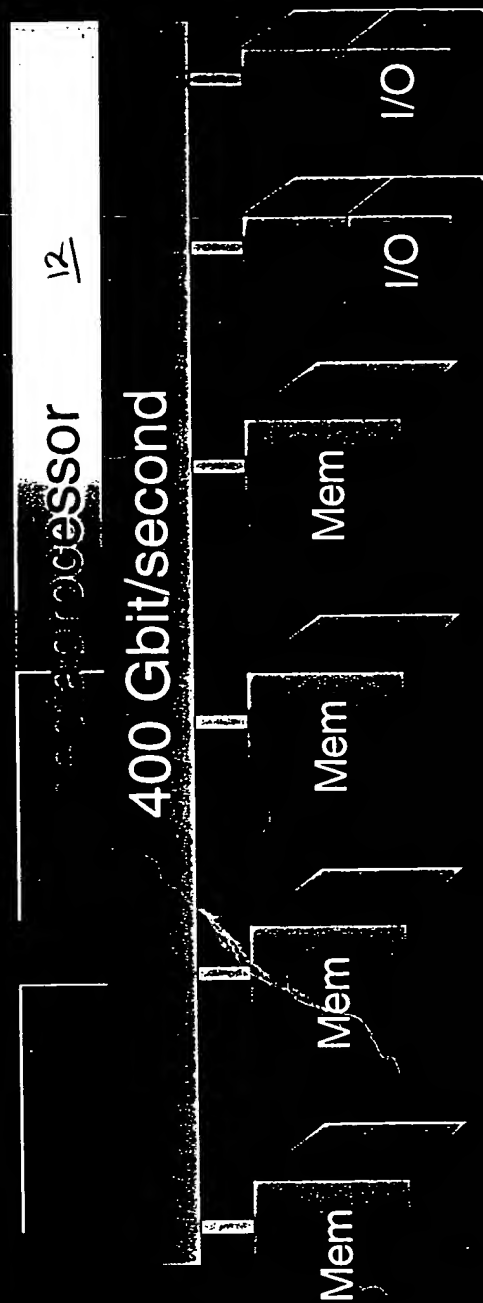
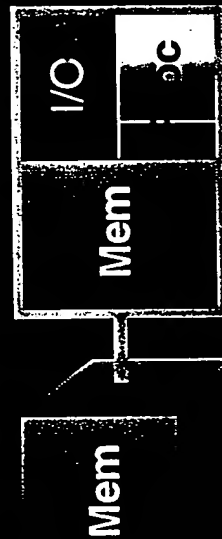


Figure 6



The Future of the Unified Way (1995)



- smaller chip
- much lower cost

Figure 7

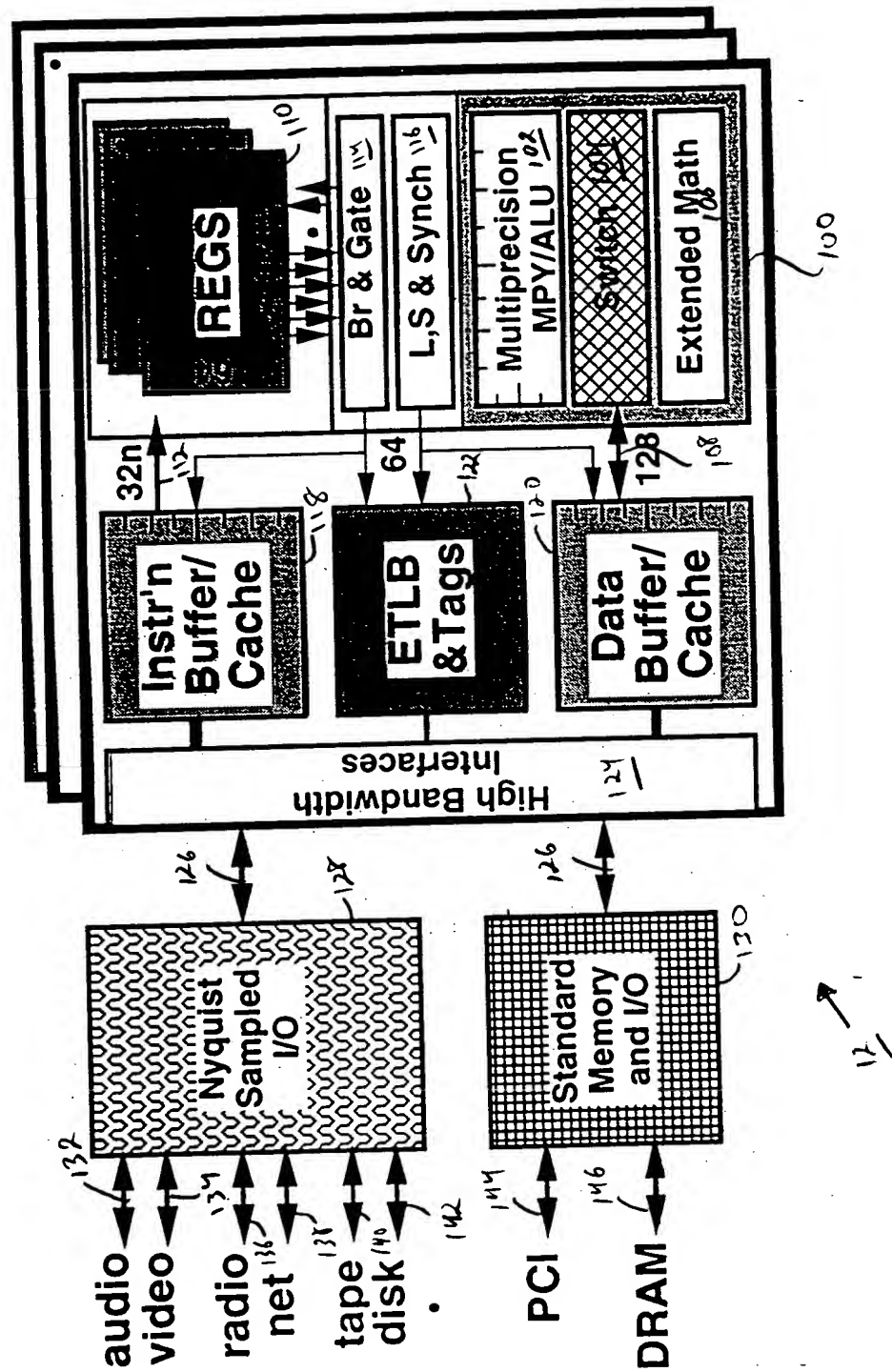


Figure 8(a)

## Group Expand

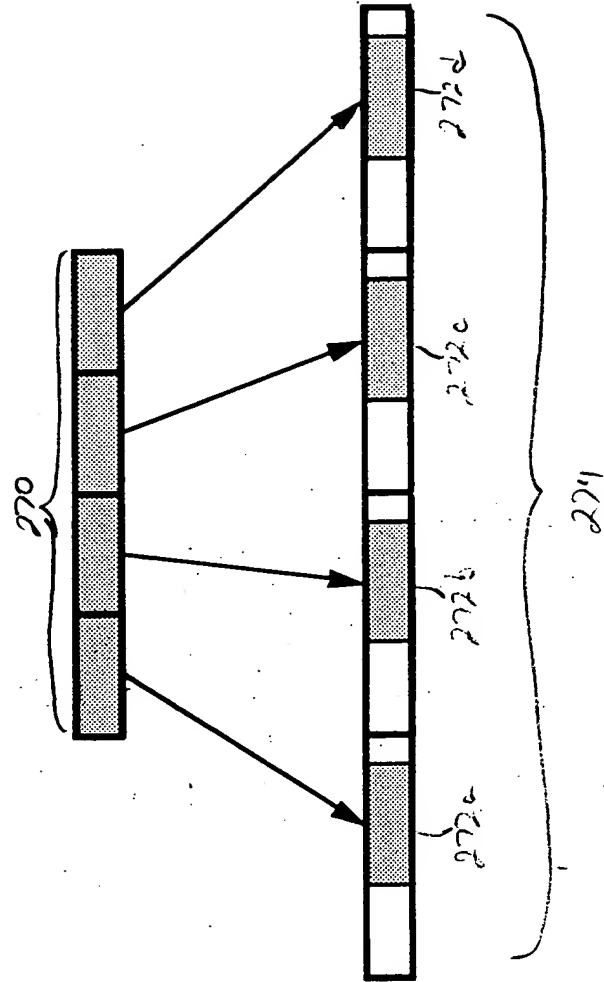




Figure 8(b)  
**Group Compress, Extract**

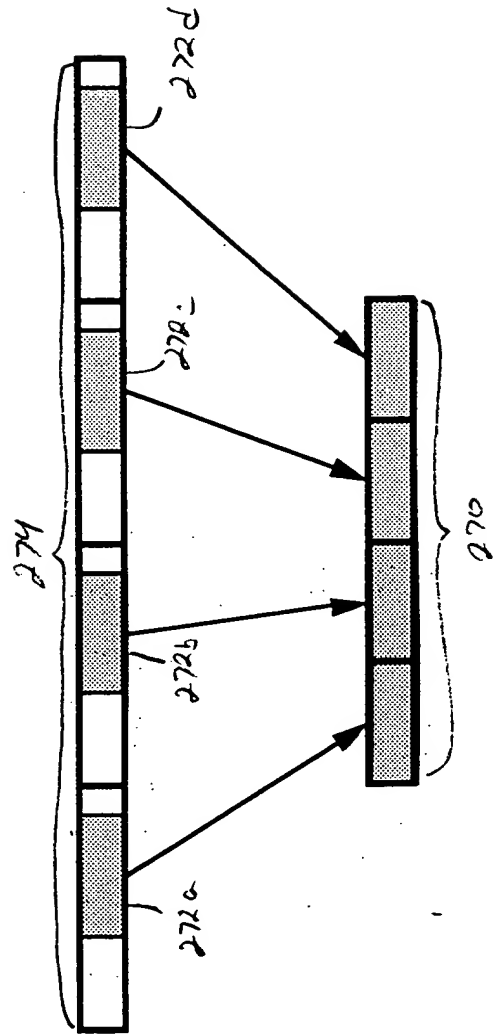
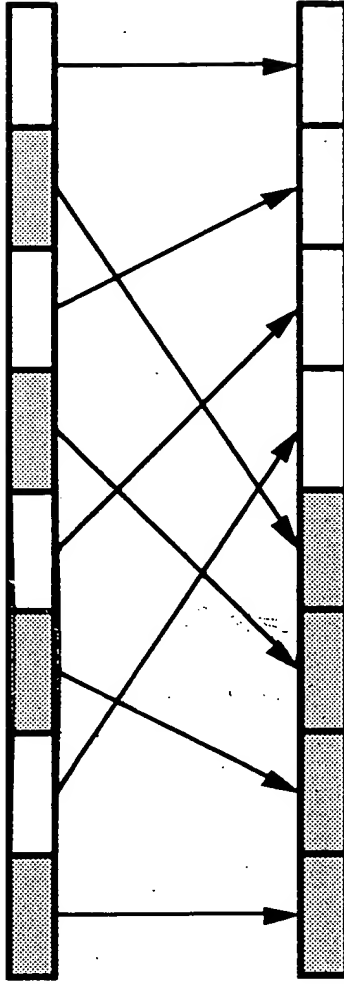


Figure 8(c)

## Group Deal, Shuffle

- Group Deal: 128 bits to 128 bits



- Group Shuffle: 128 bits to 128 bits

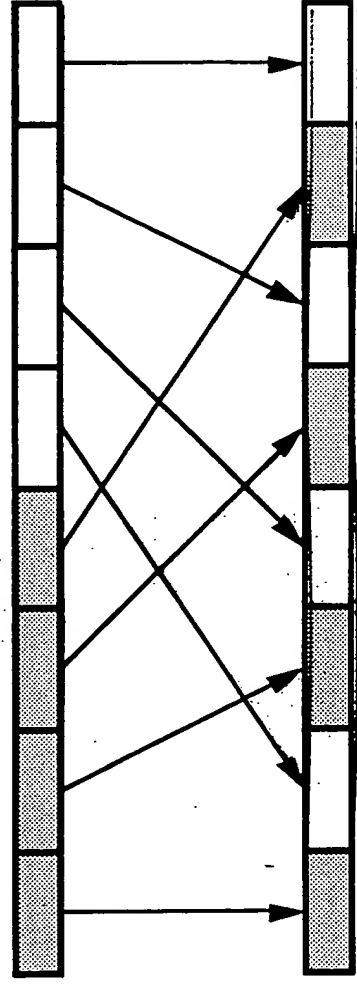


Figure 8(d)

## Group Swizzle (Copy-Swap)

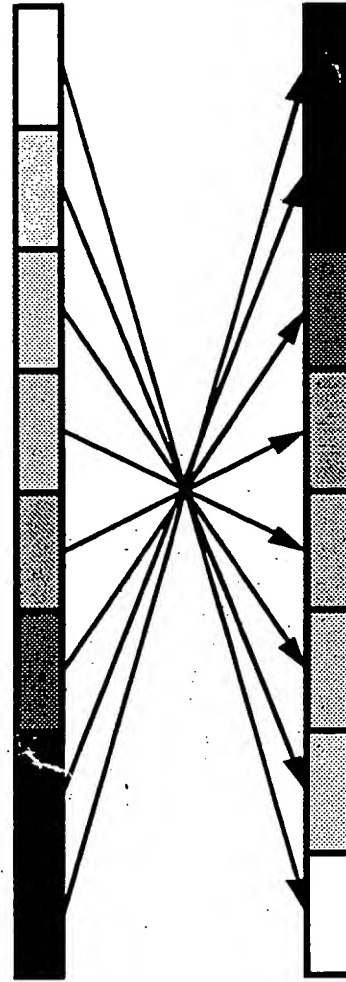


Figure 8(e)

# Group Permute

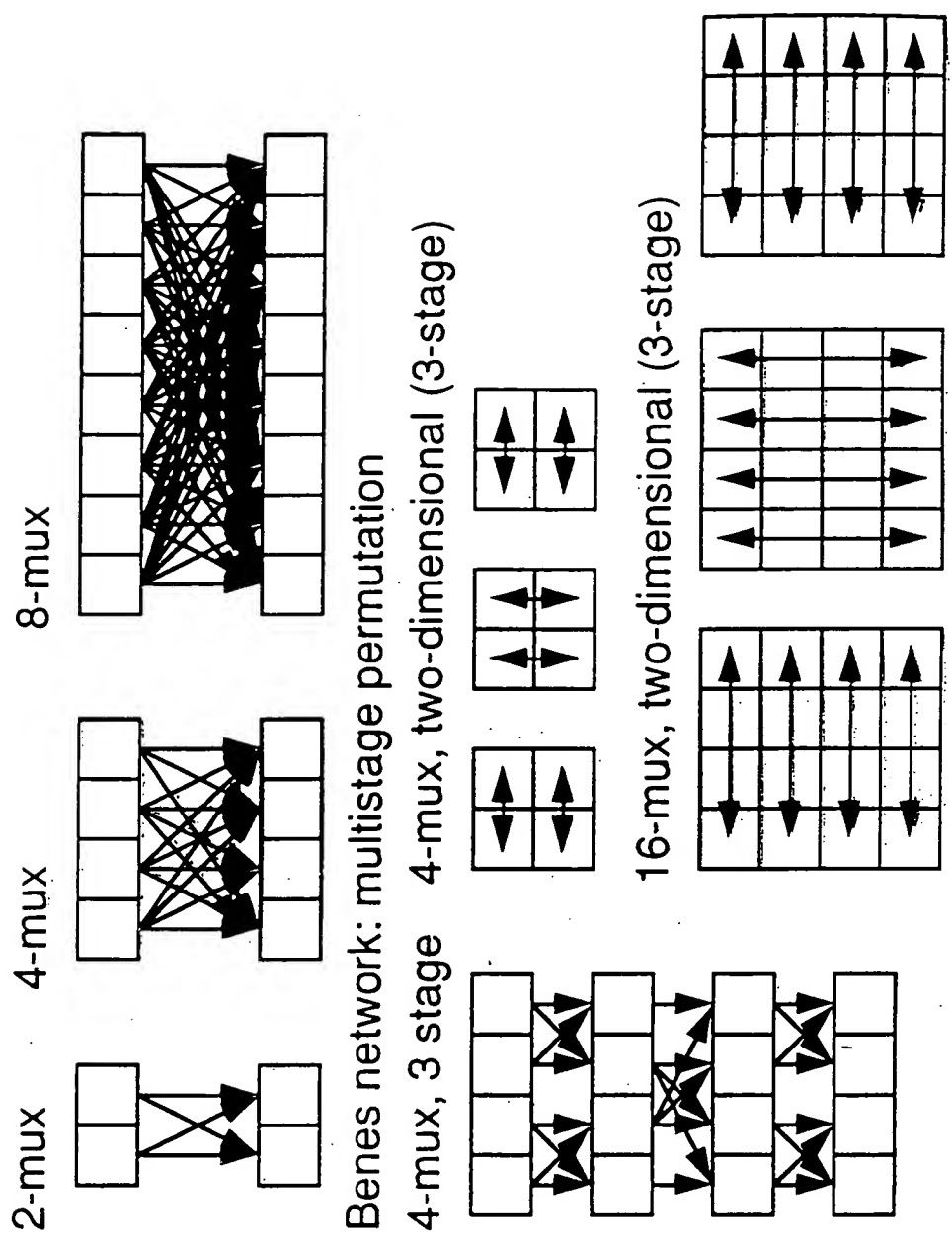


Figure 9(a)

# Instruction Formats

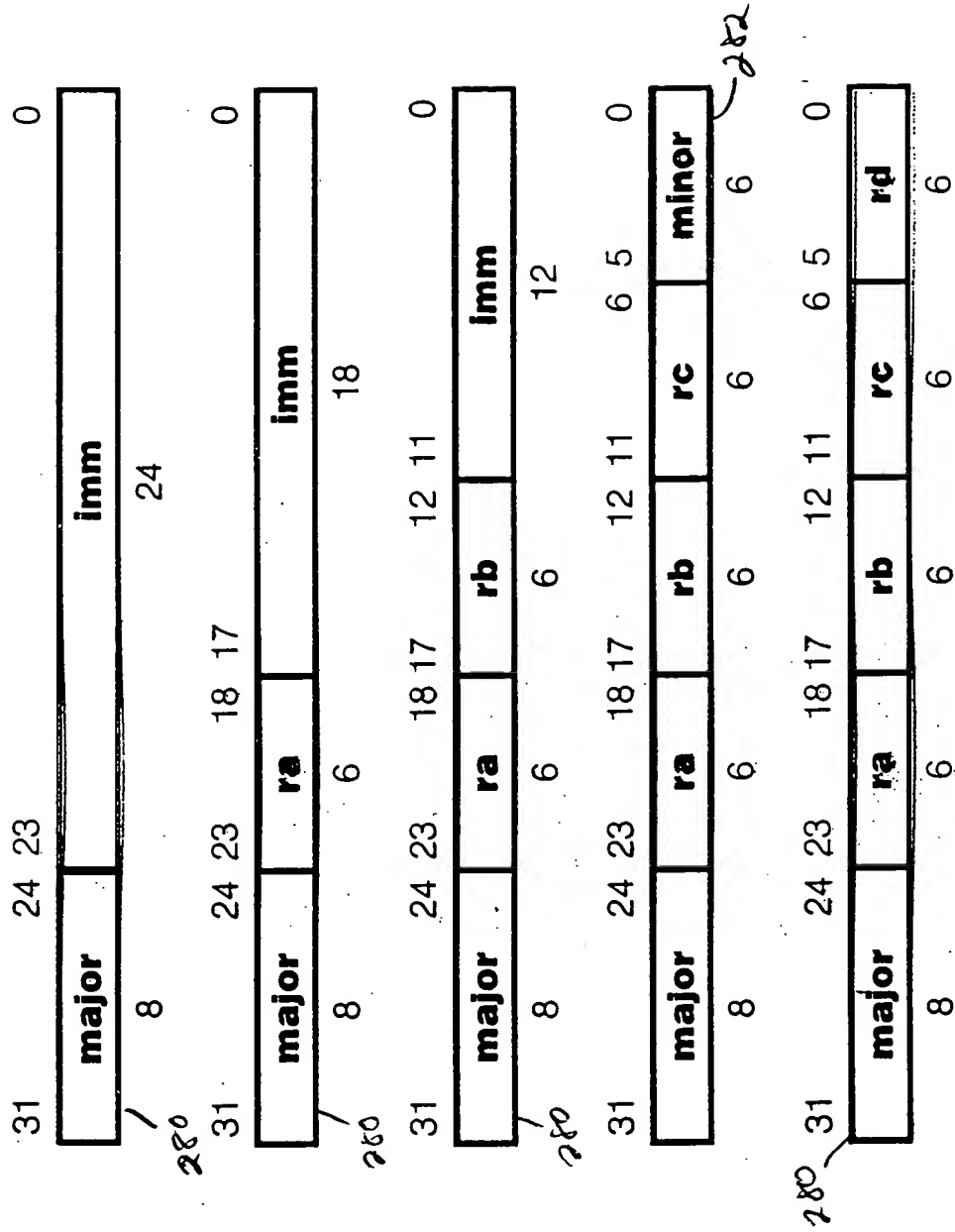


Figure 9(b)

# Floating-point Data Sizes

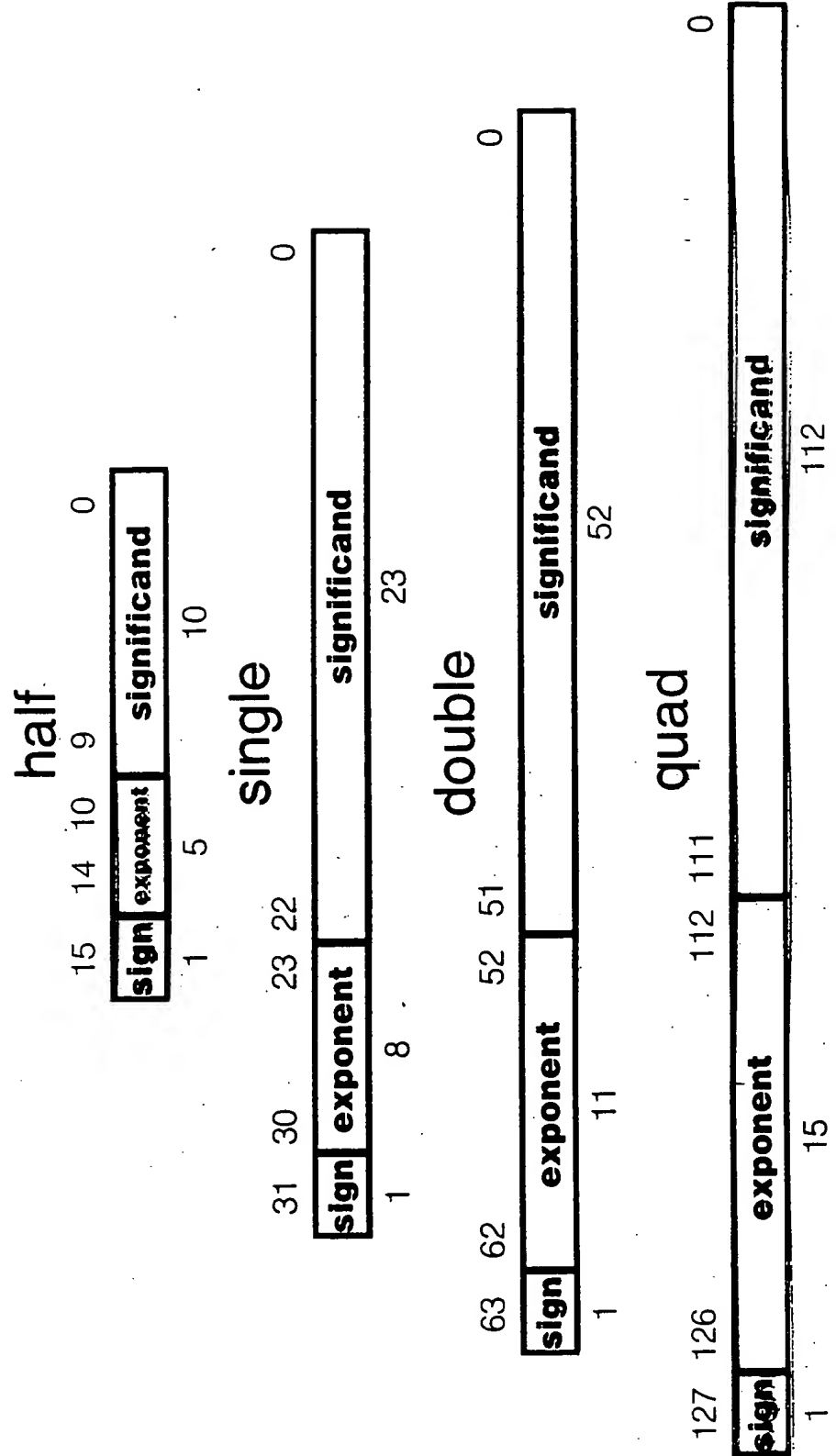


Figure 9(c)

# Fixed-Point Data Sizes

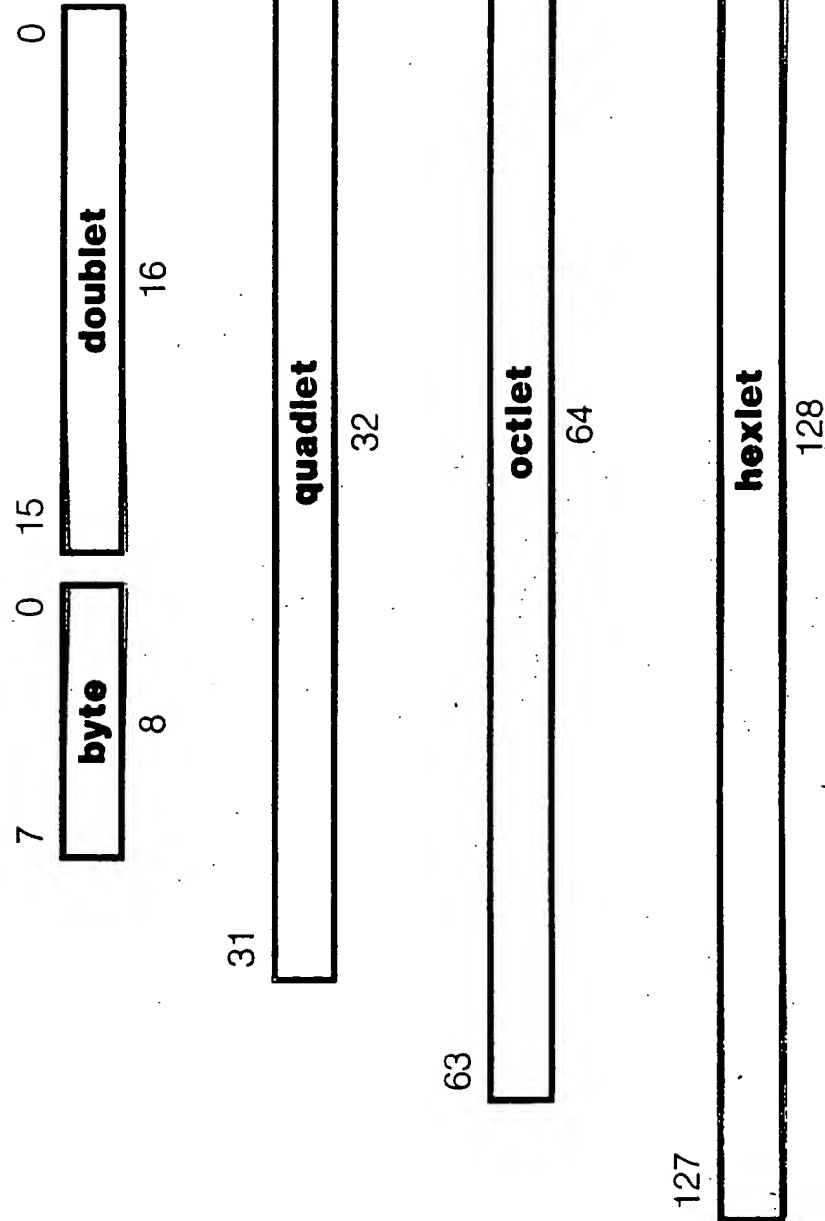


Figure 10(a)

# Translation Block Diagram

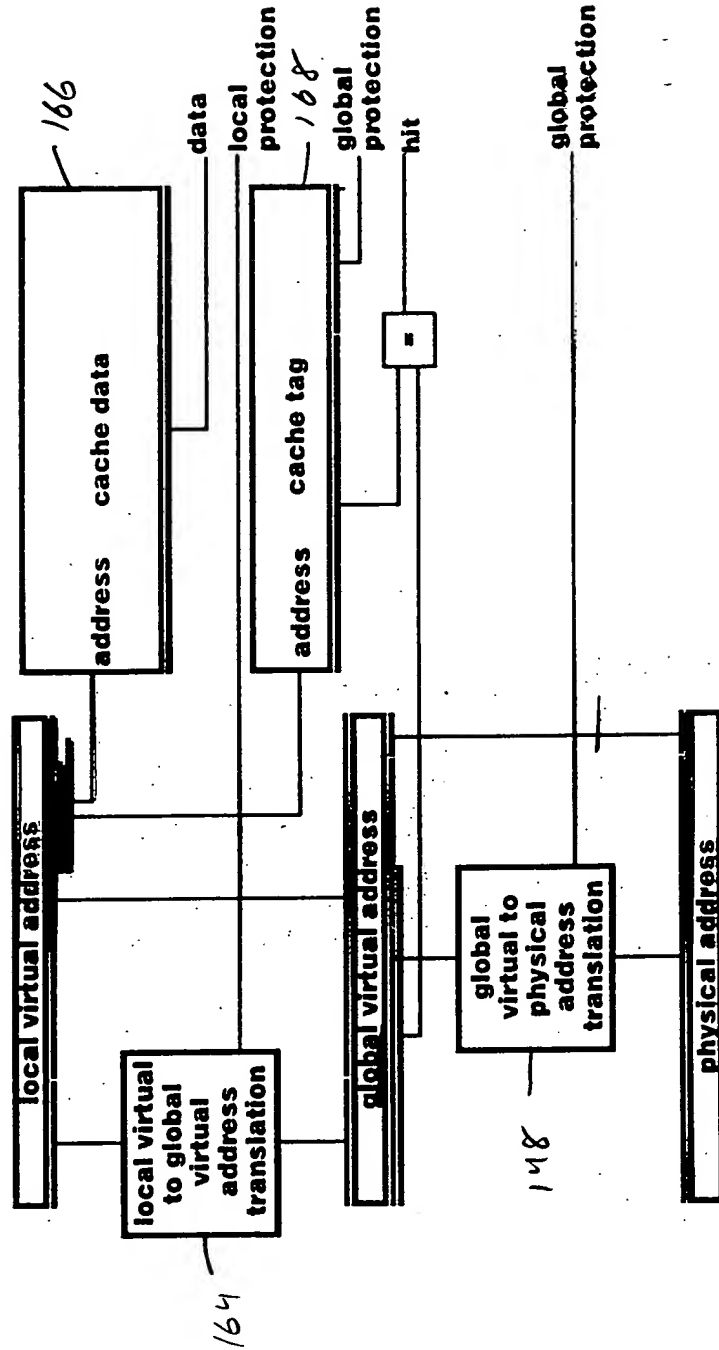




Figure 10(b)

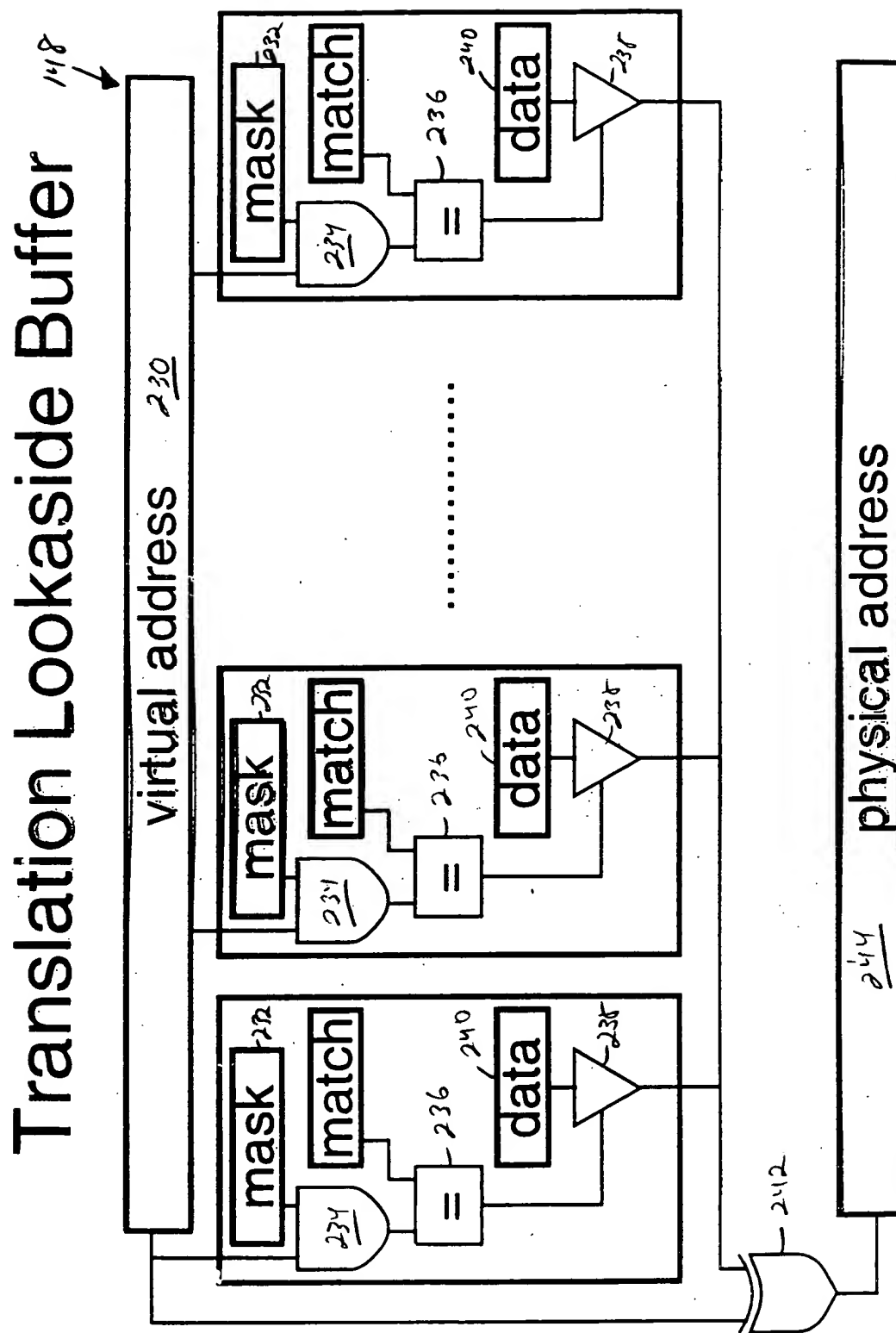


Figure 11

# Superstring Pipeline

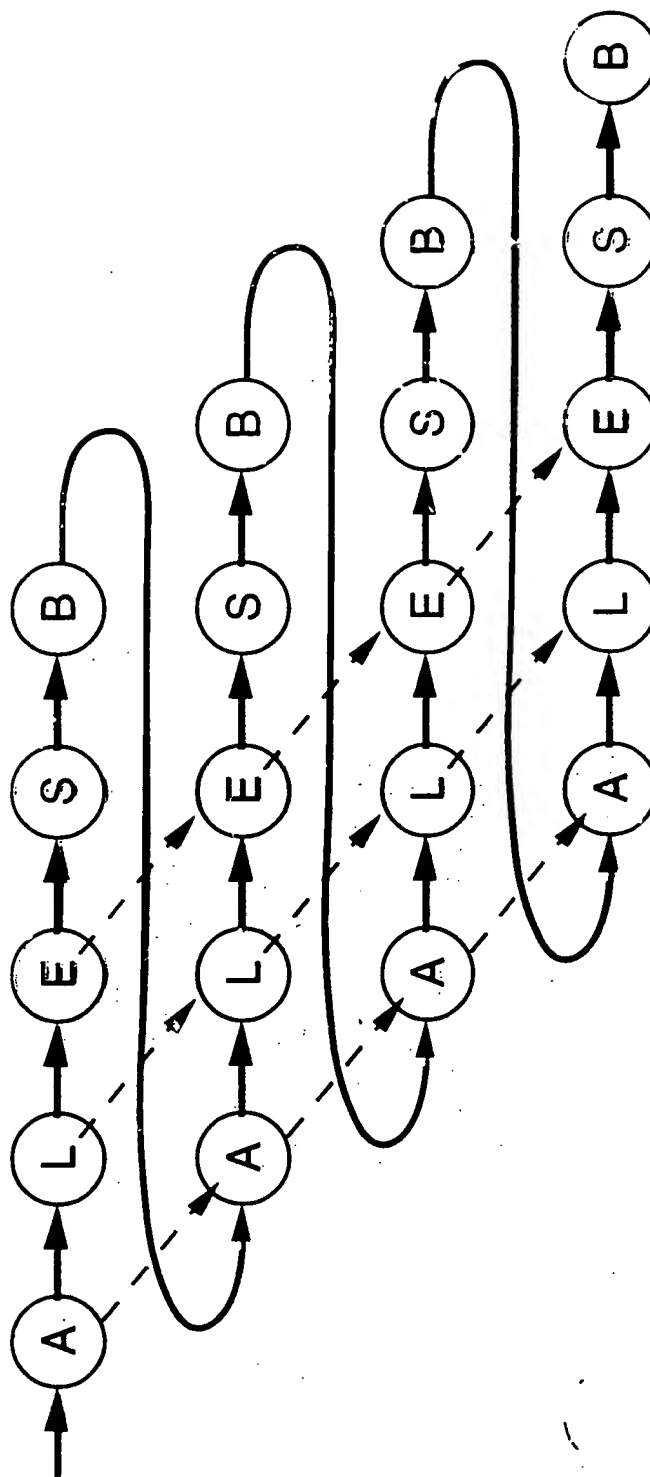


Figure 12

# Superspring Pipeline

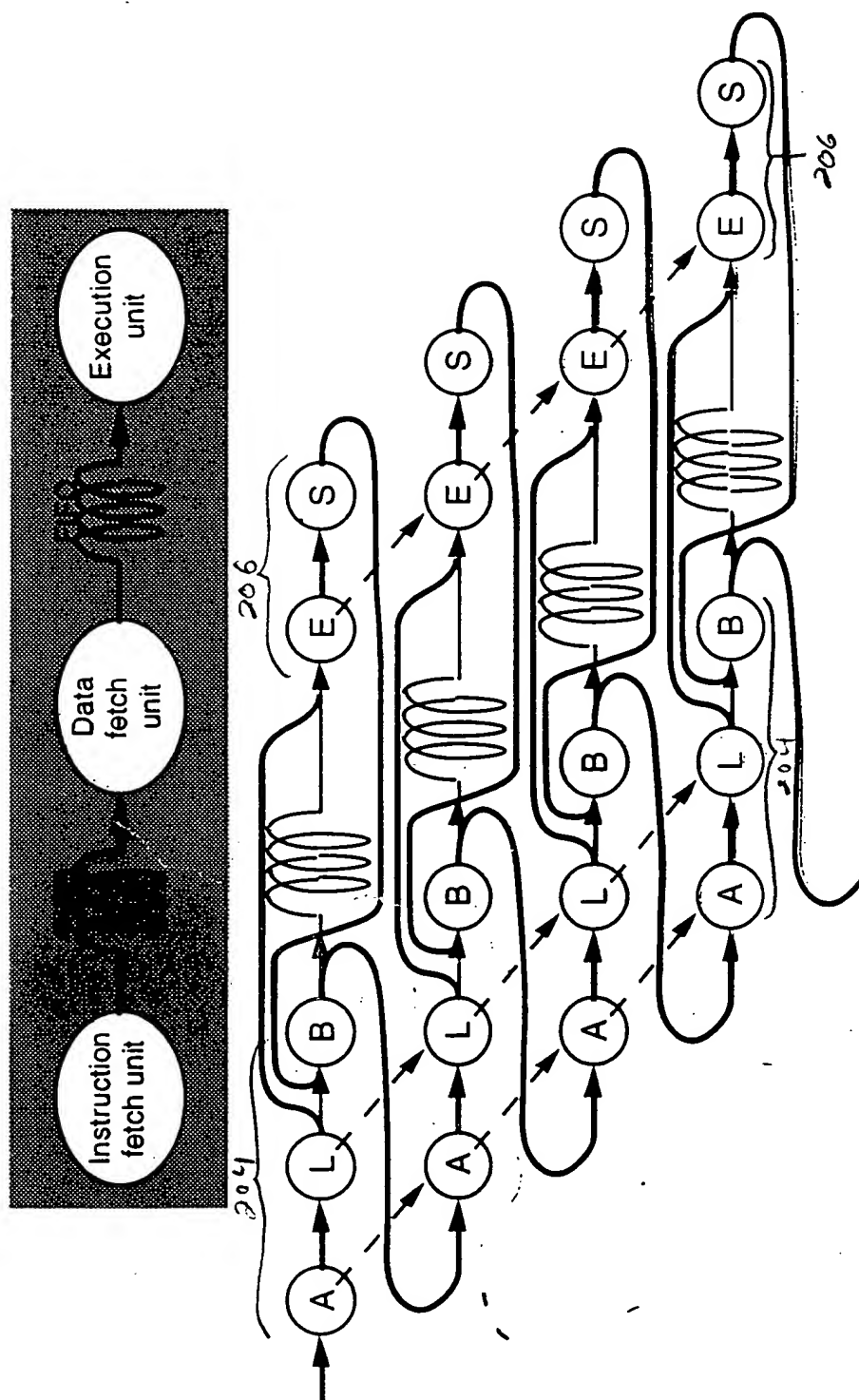


Figure 13

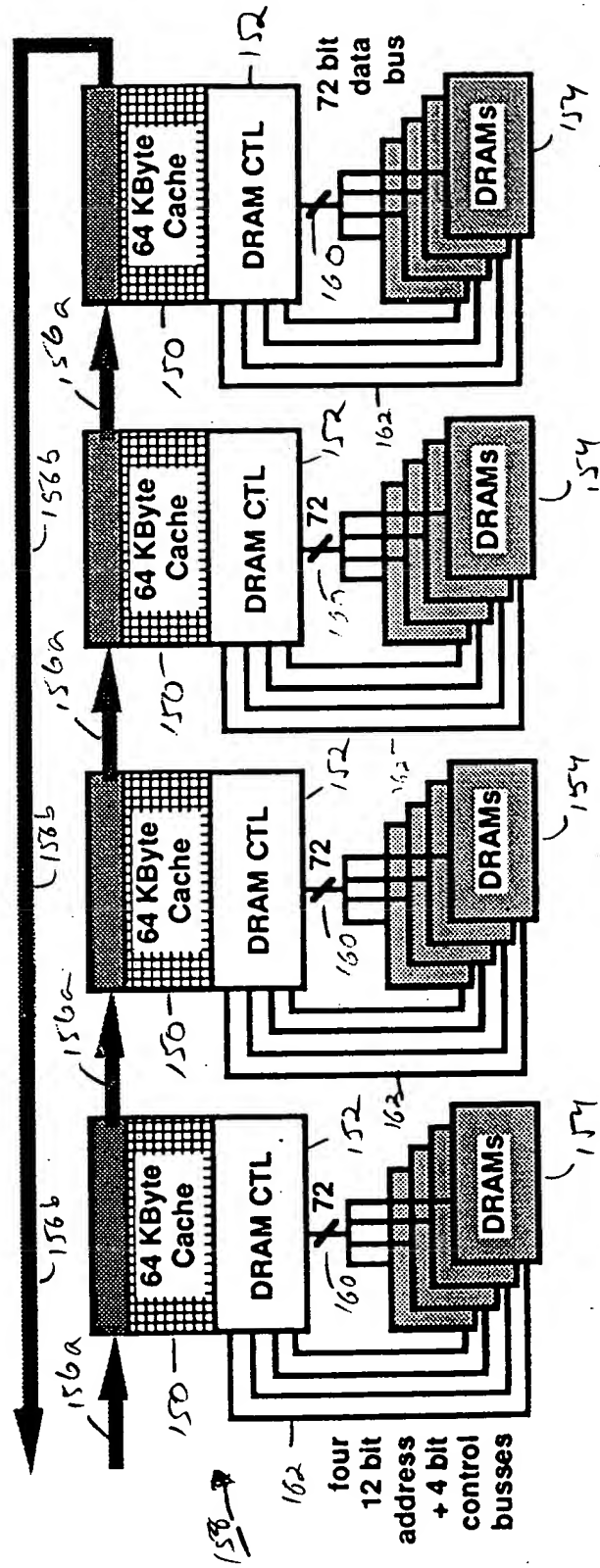


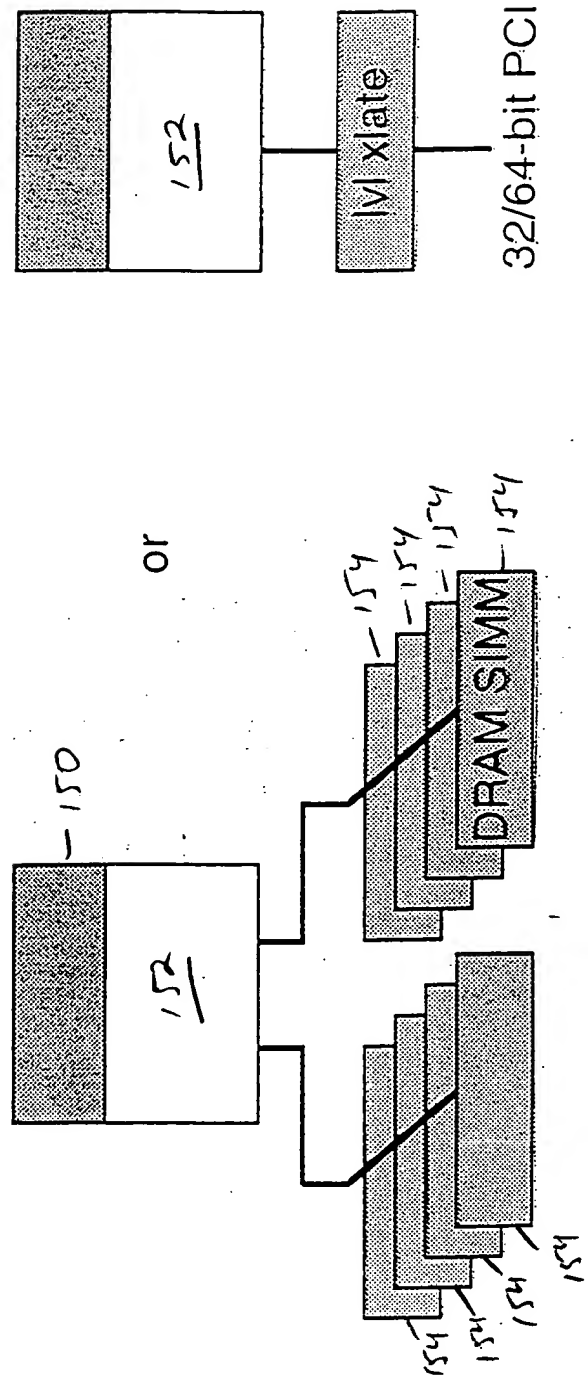
Figure 14

Figure 15

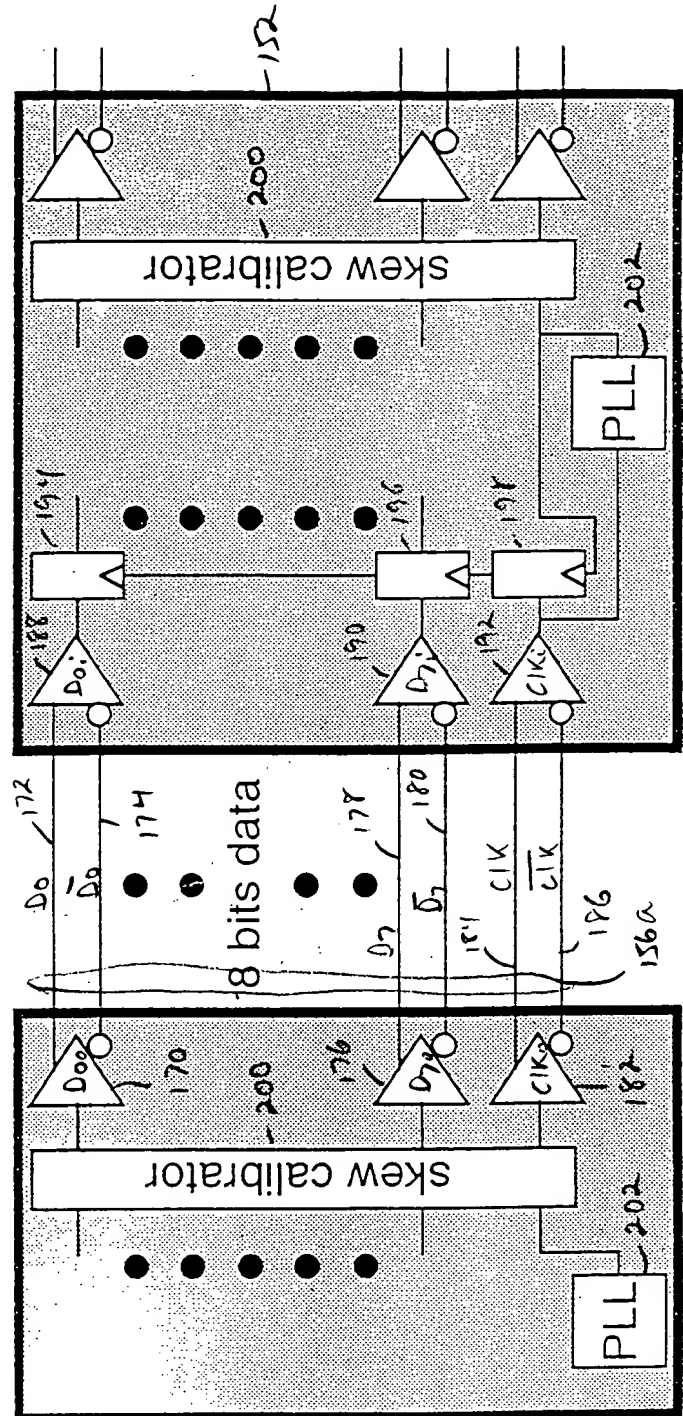


Figure 16(a)

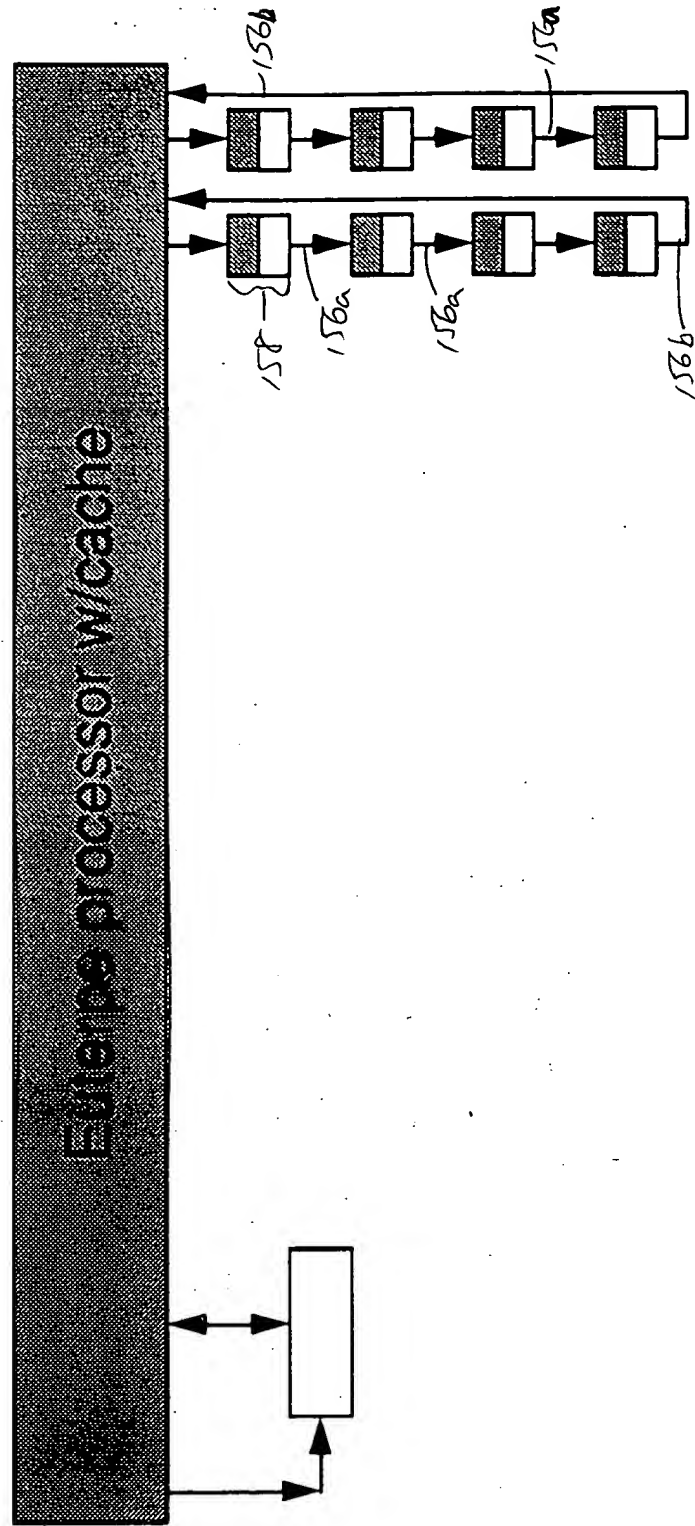


Figure 166b)

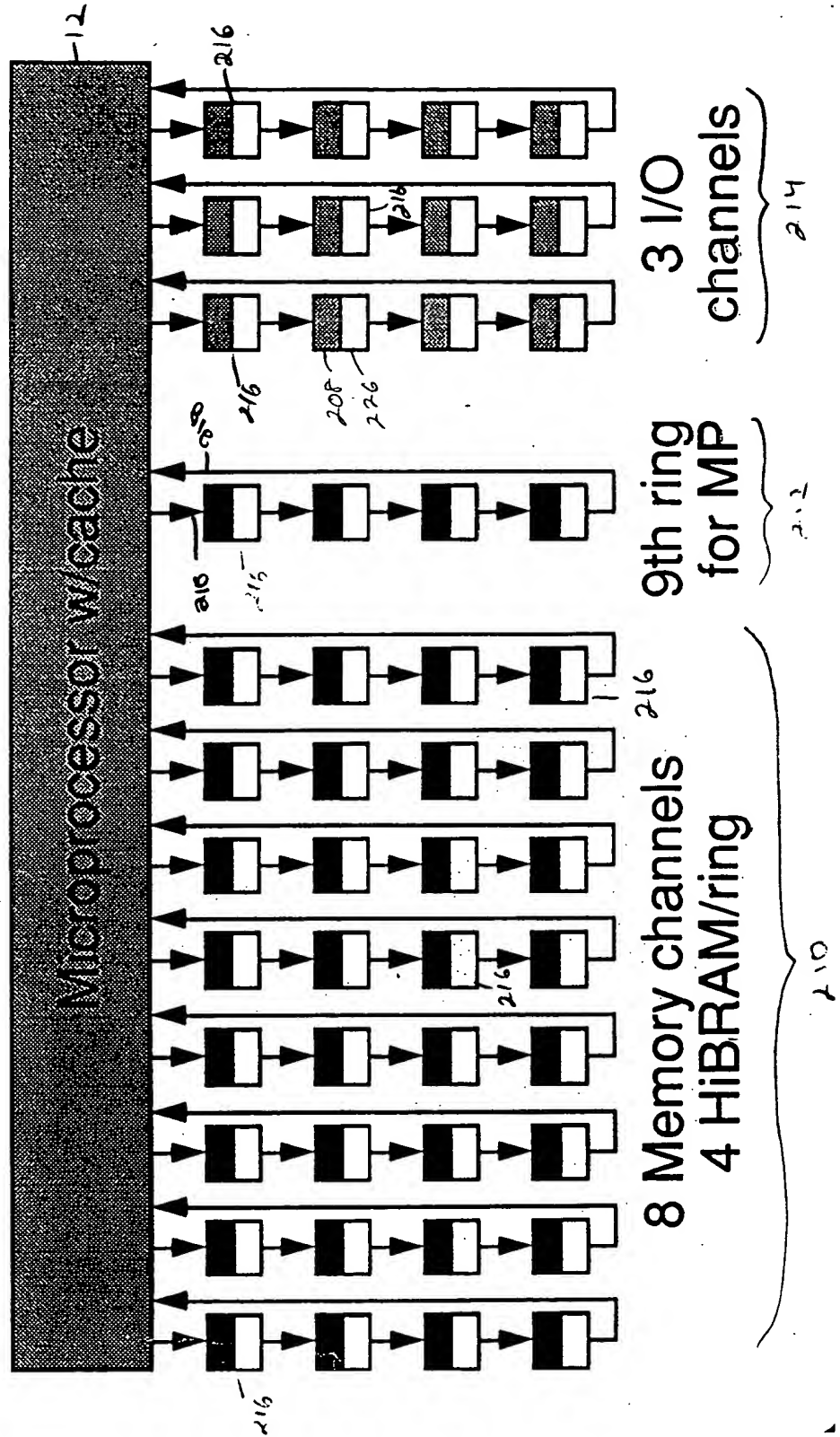




Figure 17

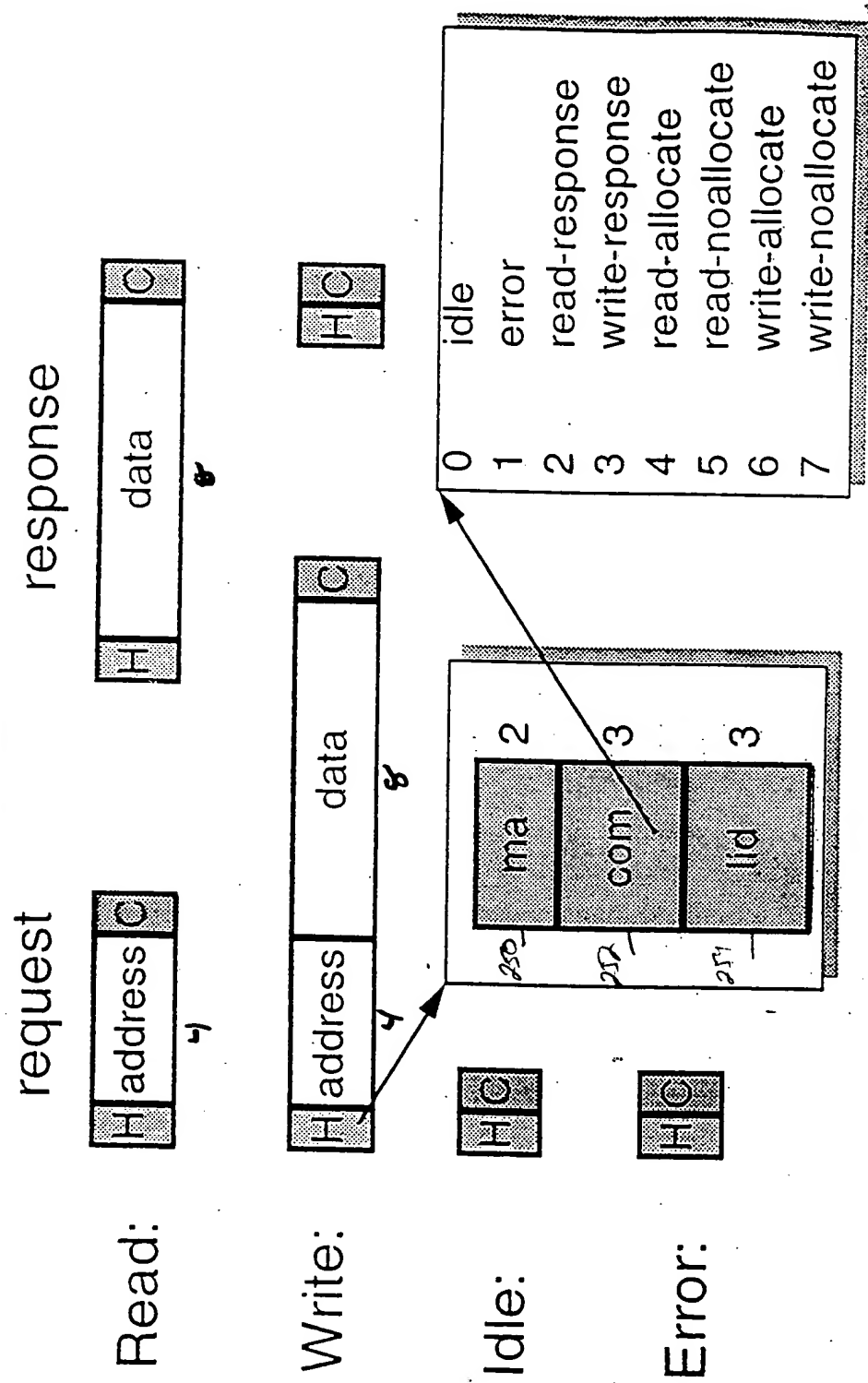


Figure 18

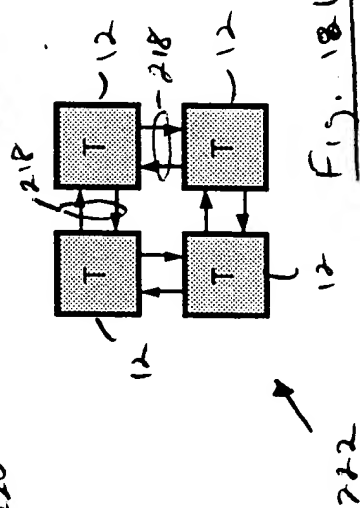
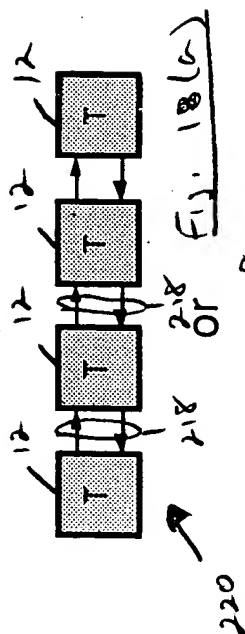


FIG. 18(b)

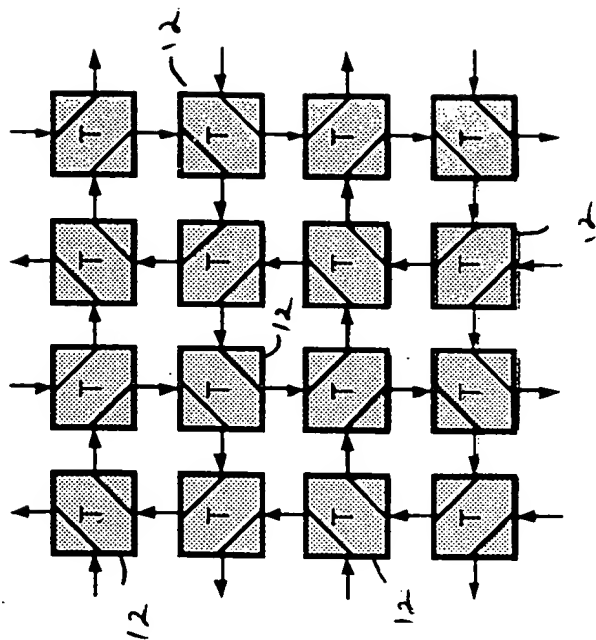
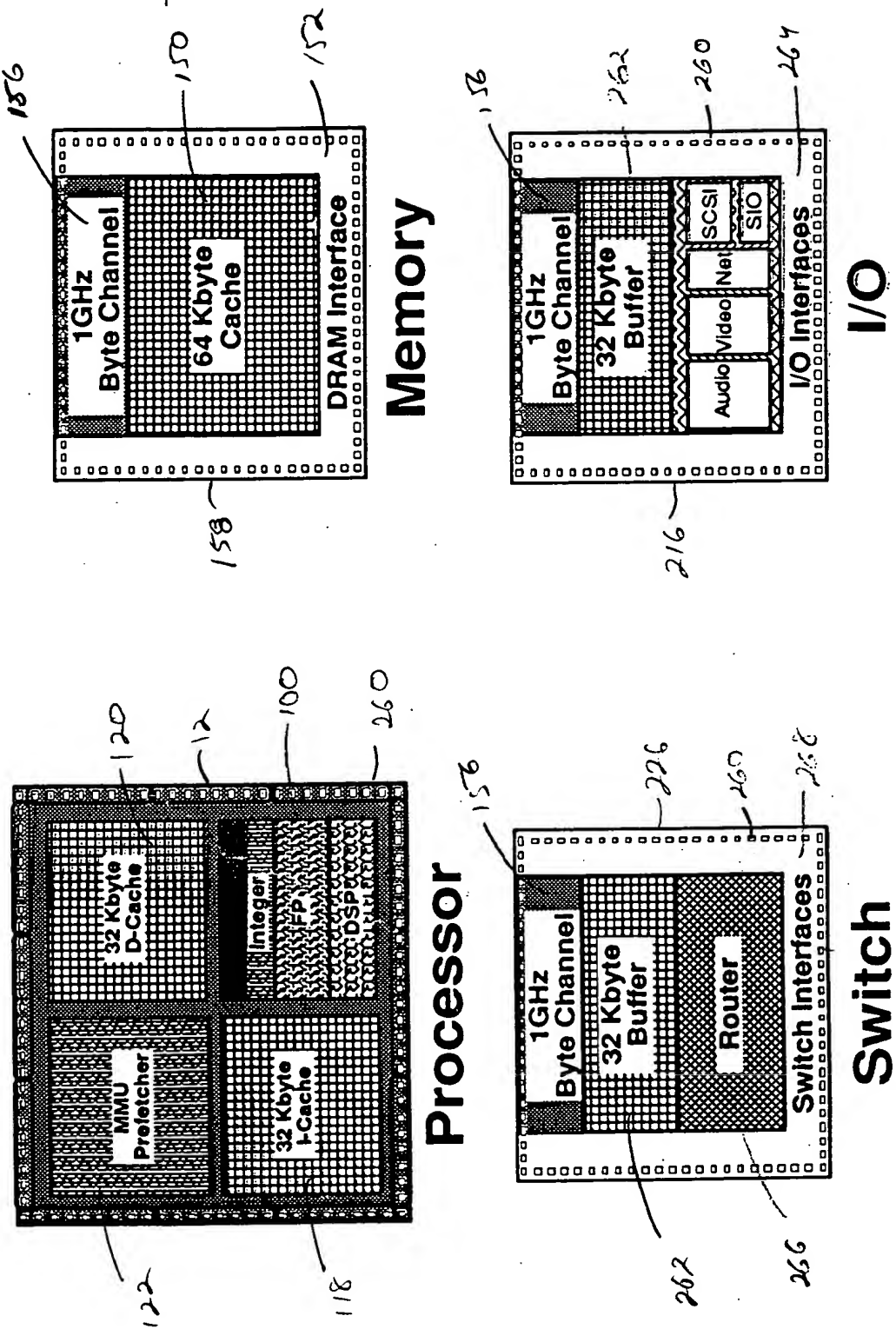


FIG. 18(c)

Figure 19



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